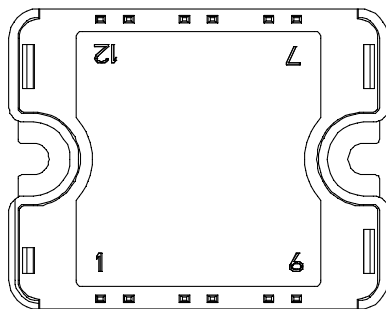
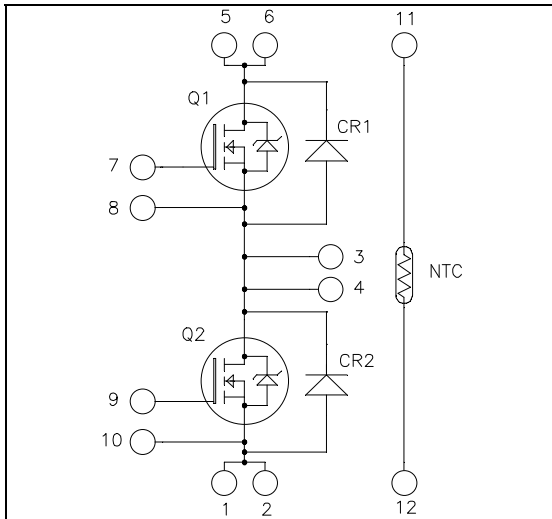


**Phase leg  
SiC MOSFET Power Module**

**$V_{DSS} = 1200V$   
 $R_{DS(on)} = 50m\Omega \text{ max @ } T_j = 25^\circ C$   
 $I_D = 74A \text{ @ } T_c = 25^\circ C$**



Pins 1/2 ; 3/4 ; 5/6 must be shorted together

**Application**

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

**Features**

- **SiC Power MOSFET**
  - Low  $R_{DS(on)}$
  - High temperature performance
- **SiC Schottky Diode**
  - Zero reverse recovery
  - Zero forward recovery
  - Temperature Independent switching behavior
  - Positive temperature coefficient on VF
- Very low stray inductance
- Internal thermistor for temperature monitoring
- Kelvin source for easy drive
- AlN substrate for improved thermal performance

**Benefits**

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

**All ratings @  $T_j = 25^\circ C$  unless otherwise specified**

**Absolute maximum ratings (Per SiC MOSFET)**

Symbol	Parameter	Max ratings	Unit
$V_{DSS}$	Drain - Source Voltage	1200	V
$I_D$	Continuous Drain Current	$T_c = 25^\circ C$	74
		$T_c = 80^\circ C$	59
$I_{DM}$	Pulsed Drain current	140	
$V_{GS}$	Gate - Source Voltage	-10/+25	V
$R_{DS(on)}$	Drain - Source ON Resistance	50	m $\Omega$
$P_D$	Power Dissipation	$T_c = 25^\circ C$	470
			W

**CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on [www.microsemi.com](http://www.microsemi.com)

**Electrical Characteristics (Per SiC MOSFET)**

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS} = 0V$ ; $V_{DS} = 1200V$		20	200	$\mu A$
$R_{DS(on)}$	Drain – Source on Resistance	$V_{GS} = 20V$ $I_D = 40A$	$T_j = 25^\circ C$	40	50	m $\Omega$
			$T_j = 175^\circ C$	70		
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$ ; $I_D = 2mA$	1.7	3		V
$I_{GSS}$	Gate – Source Leakage Current	$V_{GS} = 20V$ , $V_{DS} = 0V$			200	nA

**Dynamic Characteristics (Per SiC MOSFET)**

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$C_{iss}$	Input Capacitance	$V_{GS} = 0V$		5120		pF
$C_{oss}$	Output Capacitance	$V_{DS} = 1000V$		240		
$C_{rss}$	Reverse Transfer Capacitance	$f = 1MHz$		40		
$Q_g$	Total gate Charge	$V_{GS} = -5/20V$ $V_{Bus} = 600V$ $I_D = 40A$		272		nC
$Q_{gs}$	Gate – Source Charge			80		
$Q_{gd}$	Gate – Drain Charge			80		
$T_{d(on)}$	Turn-on Delay Time	$V_{GS} = -5/+20V$ $V_{Bus} = 800V$ $I_D = 40A$ $R_{Gext} = 2.5\Omega$		10		ns
$T_r$	Rise Time			10		
$T_{d(off)}$	Turn-off Delay Time			45		
$T_f$	Fall Time			30		
$E_{on}$	Turn on Energy	Inductive Switching $V_{GS} = -5/+20V$ $V_{Bus} = 600V$ $I_D = 40A$ $R_{Gext} = 2.5\Omega$	$T_j = 150^\circ C$	0.9		mJ
$E_{off}$	Turn off Energy		$T_j = 150^\circ C$	0.5		mJ
$R_{Gint}$	Internal gate resistance			1.65		$\Omega$
$R_{thJC}$	Junction to Case Thermal Resistance				0.32	$^\circ C/W$

**Body diode ratings and characteristics (Per SiC MOSFET)**

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$V_{SD}$	Diode Forward Voltage	$V_{GS} = 0V$ , $I_{SD} = 40A$		3.9		V
$t_{rr}$	Reverse Recovery Time	$I_{SD} = 40A$ ; $V_{GS} = -2V$ $V_R = 800V$ ; $di_F/dt = 200A/\mu s$		140		ns
$Q_{rr}$	Reverse Recovery Charge			230		nC
$I_{rr}$	Reverse Recovery Current			4		A

**SiC diode characteristics** (Per SiC diode)

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage					1200	V
I <sub>RM</sub>	Reverse Leakage Current	V <sub>R</sub> =1200V	T <sub>j</sub> = 25°C T <sub>j</sub> = 175°C		20 1000	400	μA
I <sub>F</sub>	DC Forward Current		T <sub>C</sub> = 125°C		20		A
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 20A	T <sub>j</sub> = 25°C T <sub>j</sub> = 175°C		1.5 2.3	1.8	V
Q <sub>C</sub>	Total Capacitive Charge	I <sub>F</sub> = 20A, V <sub>R</sub> = 600V di/dt = 1000A/μs			240		nC
C	Total Capacitance	f = 1MHz, V <sub>R</sub> = 200V f = 1MHz, V <sub>R</sub> = 400V			230 170		pF
R <sub>thJC</sub>	Junction to Case Thermal Resistance					0.55	°C/W

**Temperature sensor NTC** (see application note APT0406 on www.microsemi.com).

<i>Symbol</i>	<i>Characteristic</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
R <sub>25</sub>	Resistance @ 25°C		50		kΩ
ΔR <sub>25</sub> /R <sub>25</sub>			5		%
B <sub>25/85</sub>	T <sub>25</sub> = 298.15 K		3952		K
ΔB/B	T <sub>C</sub> =100°C		4		%

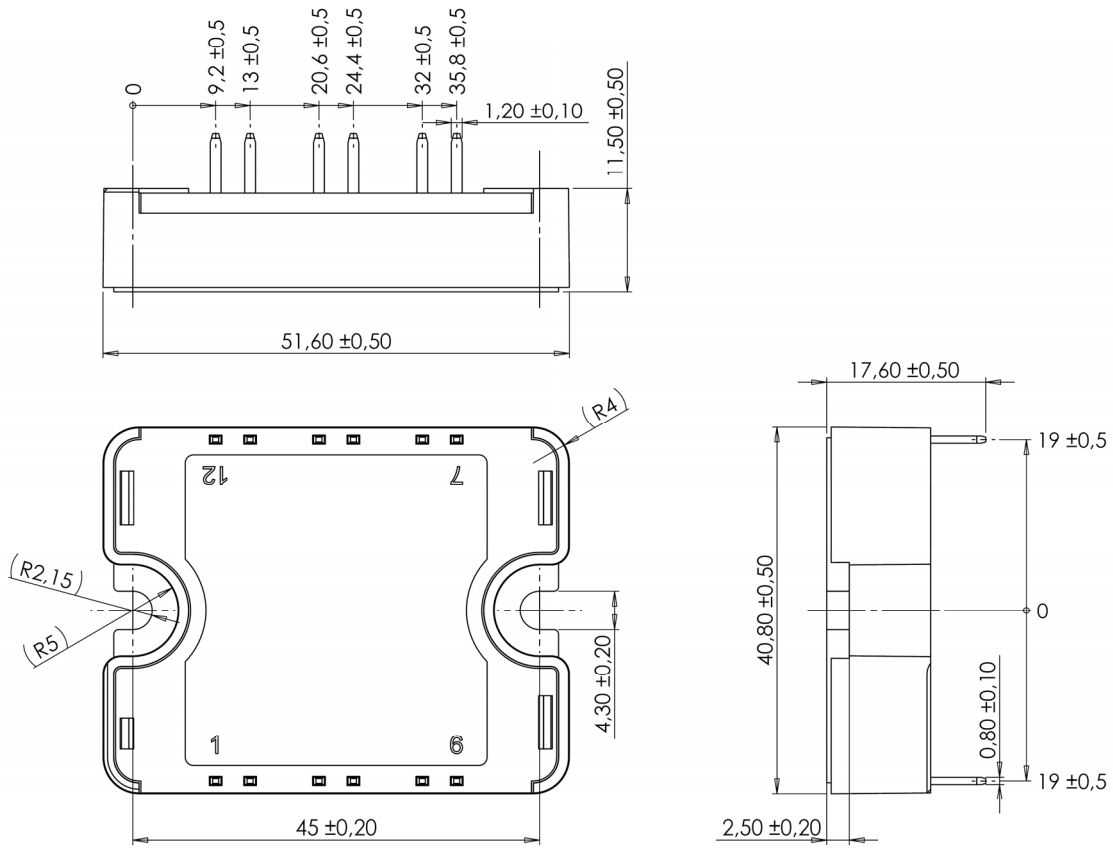
$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

T: Thermistor temperature  
R<sub>T</sub>: Thermistor value at T

**Package characteristics**

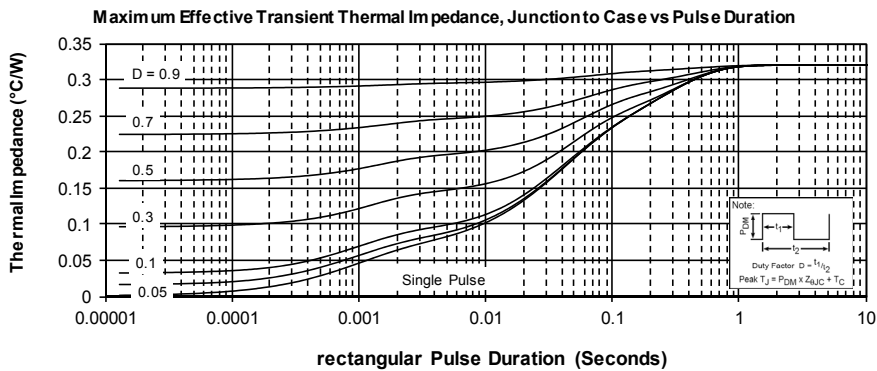
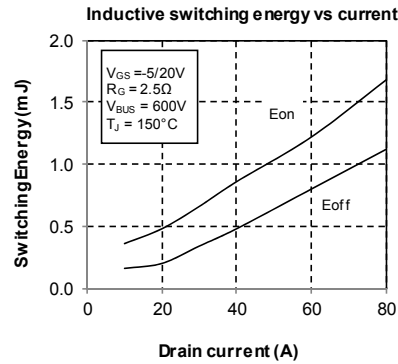
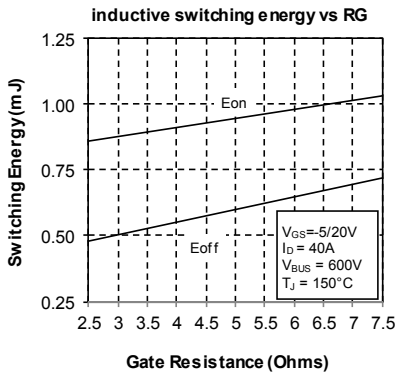
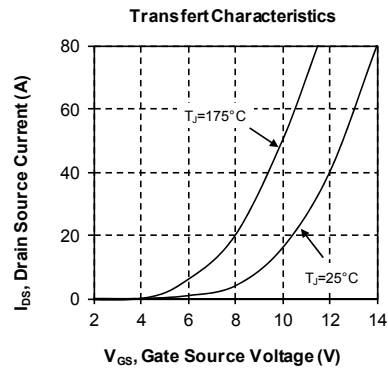
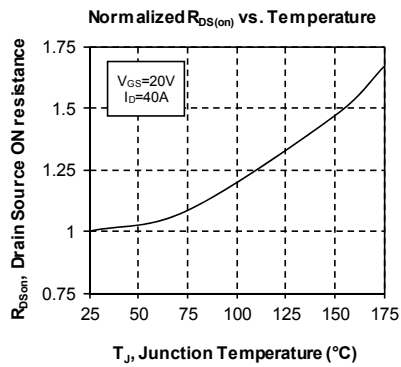
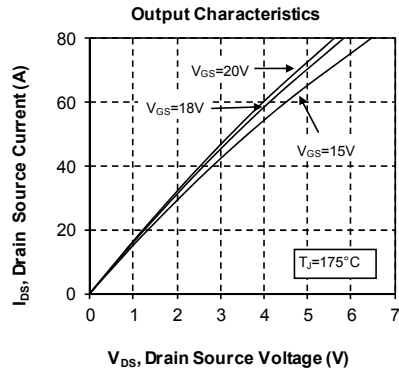
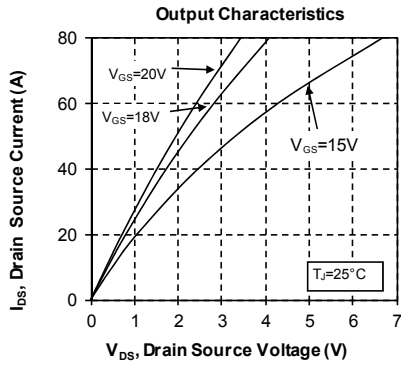
<i>Symbol</i>	<i>Characteristic</i>	<i>Min</i>	<i>Max</i>	<i>Unit</i>		
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t=1 min, 50/60Hz	4000		V		
T <sub>J</sub>	Operating junction temperature range	-40	175	°C		
T <sub>JOP</sub>	Recommended junction temperature under switching conditions	-40	T <sub>Jmax</sub> -25			
T <sub>STG</sub>	Storage Temperature Range	-40	125			
T <sub>C</sub>	Operating Case Temperature	-40	125			
Torque	Mounting torque	To heatsink	M4	2	3	N.m
Wt	Package Weight				80	g

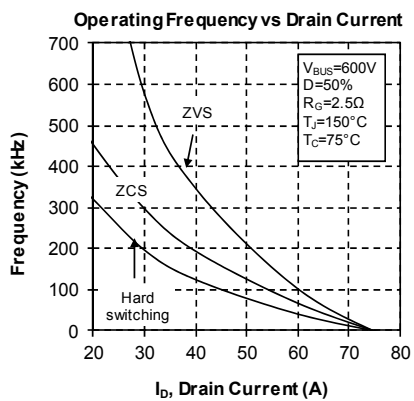
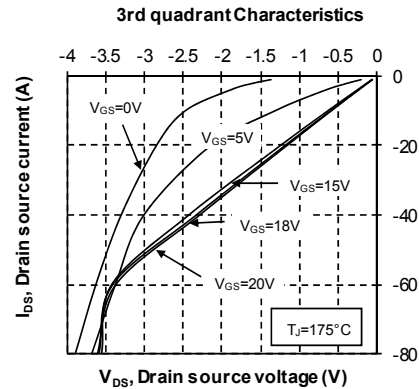
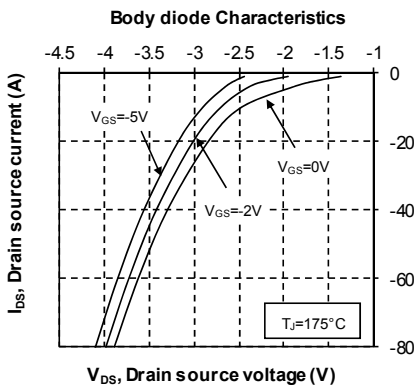
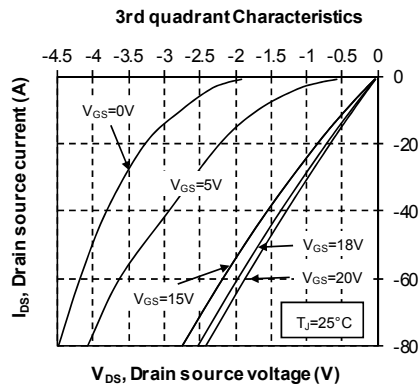
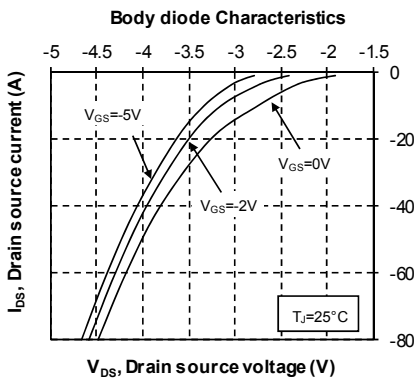
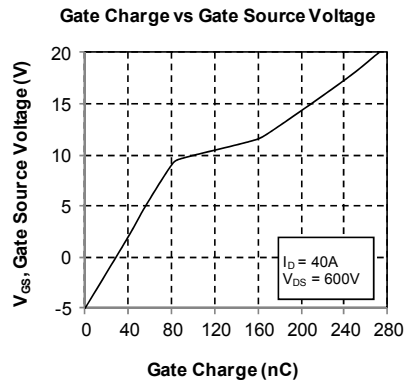
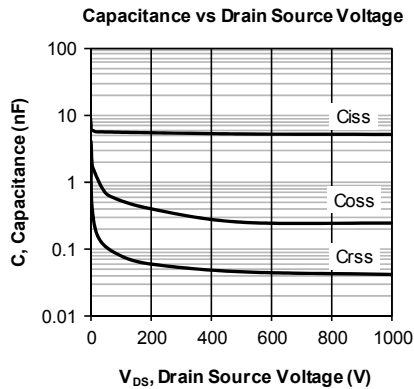
**Package outline** (dimensions in mm)



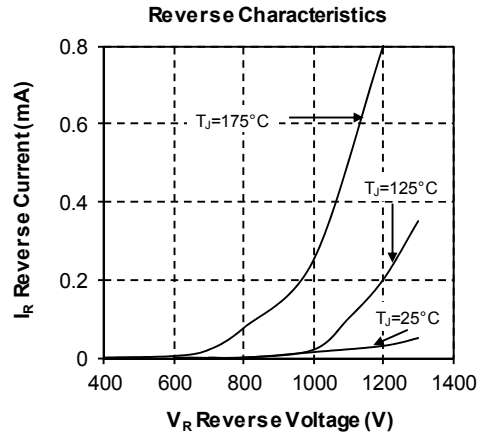
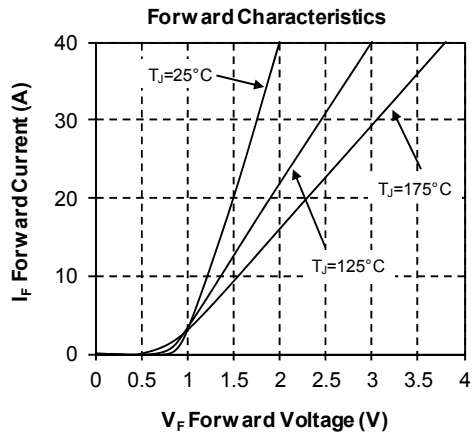
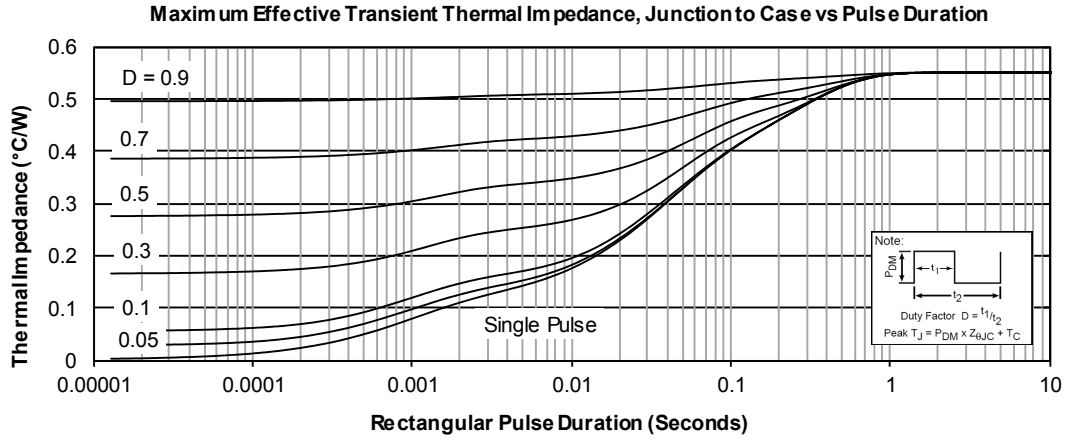
See application note 1904 - Mounting Instructions for SP1 Power Modules on [www.microsemi.com](http://www.microsemi.com)

**Typical SiC MOSFET Performance Curve**





**Typical SiC diode Performance Curve**



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