

# NOT RECOMMENDED FOR NEW DESIGN USE DMP56D0UV



DMP57D5UV

#### **DUAL P-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Features**

- Low On-Resistance
- ESD Protected Gate to 1kV
- Low Input Capacitance
- Fast Switching Speed
- Lead Free By Design/RoHS Compliant (Note 2)
- "Green" Device (Note 3)
- Qualified to AEC-Q 101 Standards for High Reliability

#### **Mechanical Data**

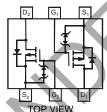
- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.006 grams (approximate)

SOT-563





TOP VIEW



TOP VIEW Internal Schematic

## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Char	acteristic	Symbol	Value	Units
Drain-Source Voltage		V <sub>DSS</sub>	-50	V
Gate-Source Voltage	Continuous	V <sub>GSS</sub>	±8	V
Drain Current (Note 2)	Continuous	I <sub>D</sub>	-160	mA

## Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 1)		$P_{D}$	400	mW
Thermal Resistance, Junction to Ambient (Note 1)	1	$R_{ heta JA}$	313	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

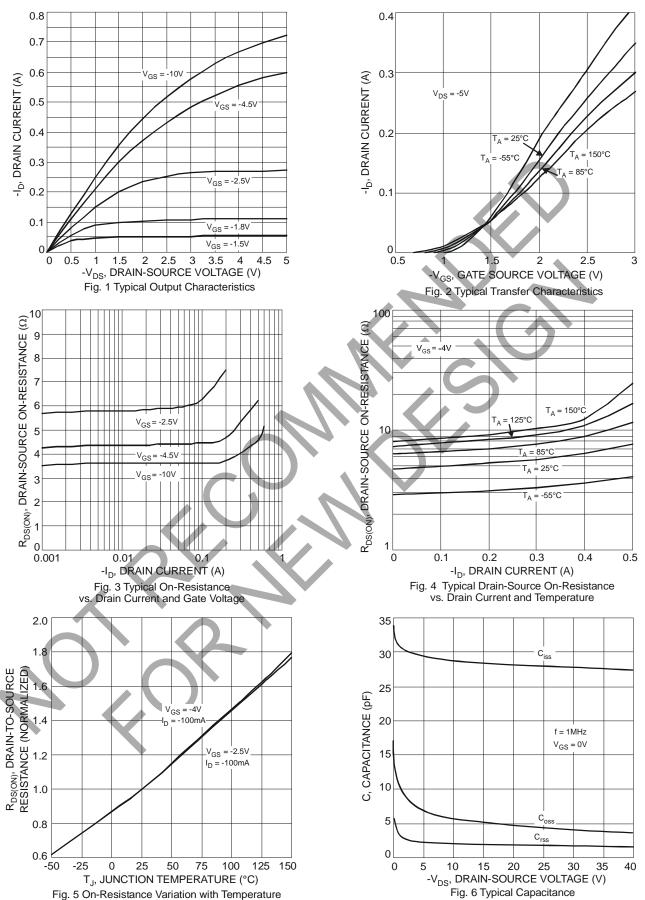
#### Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-50	_	_	V	$V_{GS} = 0V$ , $I_D = -250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			-10	μΑ	$V_{DS} = -50V, V_{GS} = 0V$
Gate-Body Leakage	I <sub>GSS</sub>	_	_	±500	nΑ	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 4)	÷.	a.				
Gate Threshold Voltage	$V_{GS(th)}$	-0.7	_	-1.0	<b>V</b>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	P (0)	_	4.6	6	Ω	$V_{GS} = -4V, I_D = -100mA$
Static Drain Podrice Of Presistance	R <sub>DS</sub> (ON)	_	6.0	8	2.2	$V_{GS} = -2.5V, I_D = -80mA$
Forward Transfer Admittance	Yfs	100	_	_	mS	$V_{DS} = -5V, I_D = -100mA$
Diode Forward Voltage	V <sub>SD</sub>	_	_	-1.2	V	$V_{GS} = 0V, I_{S} = -100mA$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C <sub>iss</sub>	_	29	_	рF	
Output Capacitance	Coss	_	7.3	_	pF	$V_{DS} = -25V$ , $V_{GS} = 0V$ , $f = 1.0MHz$
Reverse Transfer Capacitance	C <sub>rss</sub>	_	2.5	_	pF	

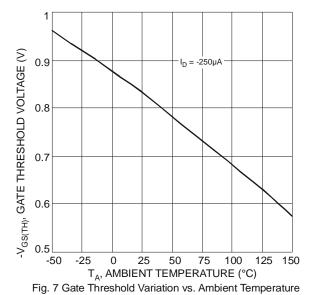
Notes:

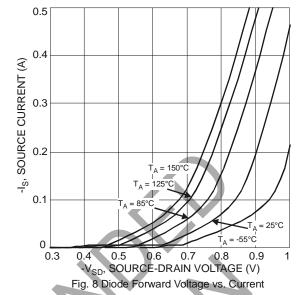
- 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 2. No purposefully added lead.
- 3. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- 4. Short duration pulse test used to minimize self-heating effect.

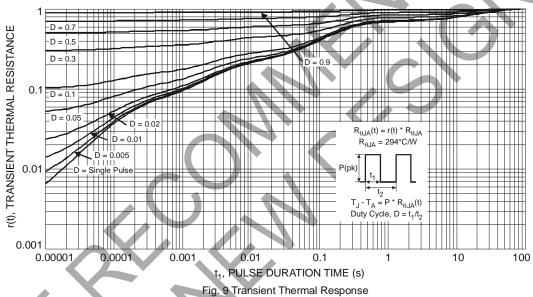










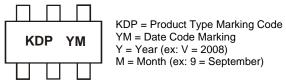


### Ordering Information (Note 5)

Part Number	Case	Packaging
DMP57D5UV -7	SOT-563	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## Marking Information (Note 6)



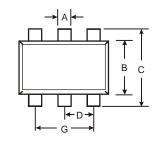
Notes: 6. Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways).

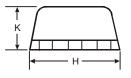
#### Date Code Key

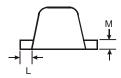
Year	2008		2009	2010		2011	2012	!	2013	2014		2015
Code	V		W	Х		Υ	Z		Α	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	j Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# **Package Outline Dimensions**

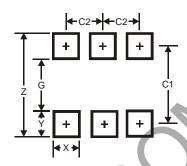






SOT-563							
Dim	Min	Max	Тур				
Α	0.15	0.30	0.20				
В	1.10	1.25	1.20				
С	1.55	1.70	1.60				
D	-	-	0.50				
G	0.90	1.10	1.00				
Н	1.50	1.70	1.60				
K	0.55	0.60	0.60				
L	0.10	0.30	0.20				
M	0.10	0.18	0.11				
All Dimensions in mm							

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Υ	0.5
C	1.7
F	0.5



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