

RoHS

COMPLIANT HALOGEN

FREE

Available

Vishay Siliconix

Dual P-Channel 25-V (G-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)			
- 30	0.023 at V _{GS} = - 10 V	- 7.6			
	0.029 at V _{GS} = - 6 V	- 6.8			

FEATURES

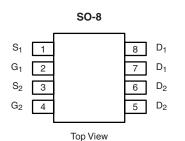
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET ٠
- 25 V V_{GS} Provides Extra Head Room for Safe • Operation
- Compliant to RoHS Directive 2002/95/EC

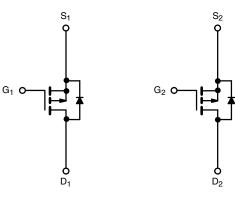
APPLICATIONS

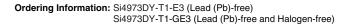
Notebook - Load Switch

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- Battery Charger Switch







P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	Γ _A = 25 °C, unle	ss otherwise n	oted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 30		V	
Gate-Source Voltage		V _{GS}	± 25			
	T _A = 25 °C	- I _D	- 7.6	- 5.8		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 6.1	- 4.6	•	
Pulsed Drain Current		I _{DM}	- 30		A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 1.7	- 0.9		
	T _A = 25 °C	– P _D	2.0	1.1	W	
Maximum Power Dissipation ^a	T _A = 70 °C		1.3	0.7		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 s	R _{thJA}	45	62.5		
Maximum Junction-to-Ambient ^a	Steady State		85	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	26	35		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static			•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 1		- 3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100		
		$V_{DS} = 0 V, V_{GS} = \pm 25 V$			± 200	n A	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V	- 1				
		V_{DS} = - 30 V, V_{GS} = 0 V, T_{J} = 55 °C			- 25	- μΑ	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 30			А	
	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 7.6 A		0.018			
Drain-Source On-State Resistance ^a		$V_{GS} = -6 V, I_D = -6.8 A$ 0.023		0.023	0.029	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 7.6 A		22		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 1.7 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Dynamic ^b	-						
Total Gate Charge	Qg			37	56	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 10 V, I_D = - 7.6 A		6			
Gate-Drain Charge	Q _{gd}			11			
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		15	25	ns	
Turn-Off Delay Time	t _{d(off)}	${ m I}_{ m D}\cong$ - 1 A, ${ m V}_{ m GEN}$ = - 10 V, ${ m R}_{ m g}$ = 6 Ω		115	180		
Fall Time	t _f			90	140		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dl/dt = 100 A/μs		80	120		

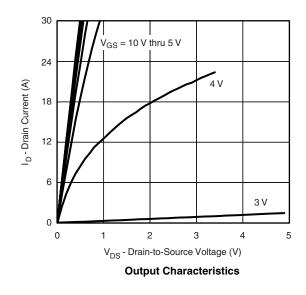
Notes:

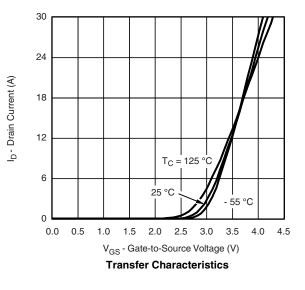
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Si4973DY

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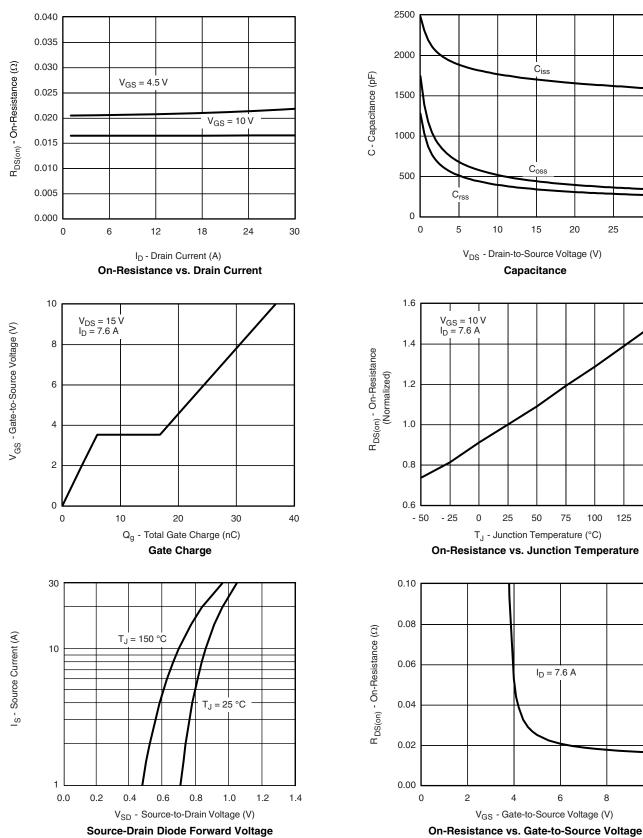
30

150

125

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

VISHAY

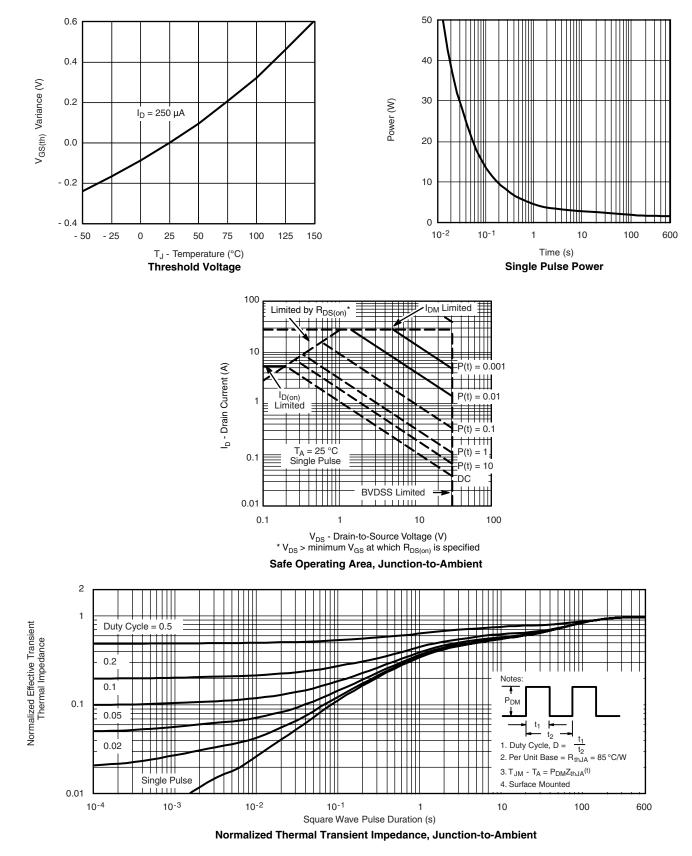


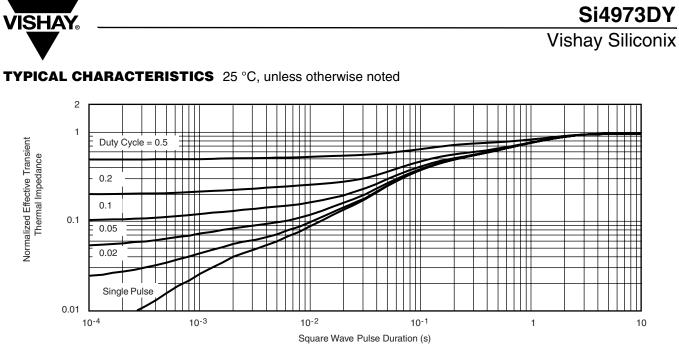
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg272164.



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