AUTOMOTIVE

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FREE

GREEN (5-2008)



# Vishay Semiconductors

### Silicon PIN Photodiode



#### **DESCRIPTION**

VEMD10940FX01 is a high speed and high sensitive PIN photodiode in a miniature side looking, surface mount package (SMD) with daylight blocking filter. Filter is matched with IR emitters operating at wavelength of 830 nm to 950 nm. The photo sensitive area of the chip is 0.23 mm<sup>2</sup>.

#### **FEATURES**

- Package type: surface mount
- Package form: Side view
- Dimensions (L x W x H in mm): 3 x 2 x 1
- AEC-Q101 qualified
- High radiant sensitivity
- Daylight blocking filter matched with 830 nm to 950 nm IR emitters
- Fast response times
- Angle of half sensitivity:  $\varphi = \pm 75^{\circ}$
- Package matched with IR emitter VSMB10940X01
- Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **APPLICATIONS**

- · High speed photo detector
- Infrared remote control
- Infrared data transmission
- · Photo interrupters
- IR touch panels
- · Automotive applications

| PRODUCT SUMMARY |                      |         |                       |  |
|-----------------|----------------------|---------|-----------------------|--|
| COMPONENT       | I <sub>ra</sub> (μΑ) | φ (deg) | λ <sub>0.5</sub> (nm) |  |
| VEMD10940FX01   | 3                    | ± 75    | 780 to 1050           |  |

#### Note

· Test conditions see table "Basic Characteristics"

| ORDERING INFORMATION |               |                              |              |  |  |
|----------------------|---------------|------------------------------|--------------|--|--|
| ORDERING CODE        | PACKAGING     | REMARKS                      | PACKAGE FORM |  |  |
| VEMD10940FX01        | Tape and reel | MOQ: 3000 pcs, 3000 pcs/reel | Side view    |  |  |

#### Note

• MOQ: minimum order quantity

| ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified) |   |                  |             |      |  |
|---|---|------------------|-------------|------|--|
| PARAMETER   | TEST CONDITION                            | SYMBOL           | VALUE       | UNIT |  |
| Reverse voltage   |   | $V_{R}$          | 60          | V    |  |
| Power dissipation   | T <sub>amb</sub> ≤ 25 °C                  | $P_V$            | 104         | mW   |  |
| Junction temperature  |   | Tj               | 100         | °C   |  |
| Operating temperature range   |   | T <sub>amb</sub> | -40 to +100 | °C   |  |
| Storage temperature range   |   | T <sub>stg</sub> | -40 to +100 | °C   |  |
| Soldering temperature   | According to reflow solder profile fig. 8 | $T_{sd}$         | 260         | °C   |  |
| Thermal resistance junction / ambient   | According to J-STD-051                    | $R_{thJA}$       | 450         | K/W  |  |

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| <b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified) |  |                   |      |             |      |      |
|---|--|-------------------|------|-------------|------|------|
| PARAMETER   | TEST CONDITION   | SYMBOL            | MIN. | TYP.        | MAX. | UNIT |
| Forward voltage   | $I_F = 50 \text{ mA}$  | $V_{F}$           | -    | 1.56        | -    | V    |
| Breakdown voltage   | $I_R = 100 \mu A, E = 0$   | V <sub>(BR)</sub> | 32   | -           | -    | V    |
| Reverse dark current  | V <sub>R</sub> = 10 V, E = 0   | I <sub>ro</sub>   | 1    | 1           | 10   | nA   |
| Diode capacitance   | $V_R = 0 V, f = 1 MHz, E = 0$  | $C_D$             | 1    | 3.3         | Ī    | pF   |
|   | $V_R = 3 \text{ V, f} = 1 \text{ MHz, E} = 0$                                | $C_D$             | -    | 1.5         | ı    | pF   |
| Open circuit voltage  | $E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$                          | Vo                | -    | 350         | -    | mV   |
| Temperature coefficient of Vo   | $E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$                          | $TK_Vo$           | 1    | -2.7        | Ī    | mV/K |
| Short circuit current   | $E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$                          | l <sub>k</sub>    | -    | 2.8         | ı    | μΑ   |
| Temperature coefficient of Ik   | $E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$                          | TK <sub>lk</sub>  | -    | 0.4         | -    | %/K  |
| Reverse light current   | $E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$ , $V_R = 5 \text{ V}$ | I <sub>ra</sub>   | 2    | 3           | 4    | μΑ   |
| Angle of half sensitivity   |  | φ                 | -    | ± 75        | ı    | deg  |
| Wavelength of peak sensitivity  |  | $\lambda_{p}$     | -    | 950         | -    | nm   |
| Range of spectral bandwidth   |  | λ <sub>0.5</sub>  | -    | 780 to 1050 | Ī    | nm   |
| Rise time   | $V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$      | t <sub>r</sub>    | -    | 100         | -    | ns   |
| Fall time   | $V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$      | t <sub>f</sub>    | -    | 100         | -    | ns   |

### **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

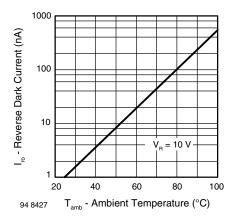


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

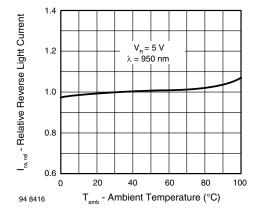


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

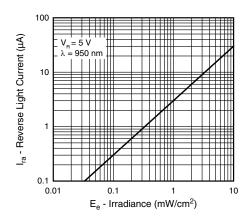


Fig. 3 - Reverse Light Current vs. Irradiance

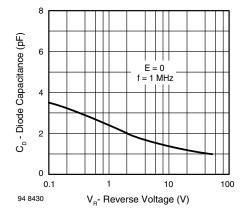


Fig. 4 - Diode Capacitance vs. Reverse Voltage

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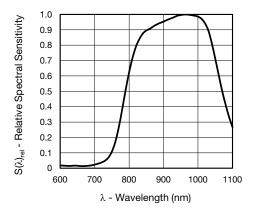


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

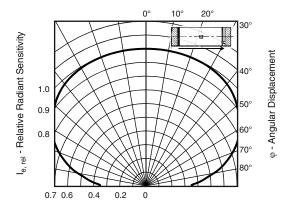


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement-Horizontal

**REFLOW SOLDER PROFILE** 

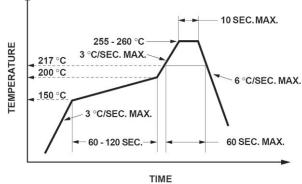


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020D

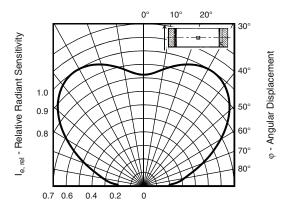


Fig. 7 - Relative Radiant Sensitivity vs. Angular Displacement -Vertical

#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

Moisture sensitivity level 3, acc. to J-STD-020.

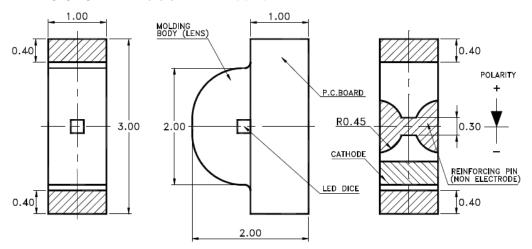
#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+5 °C), RH < 5 %.

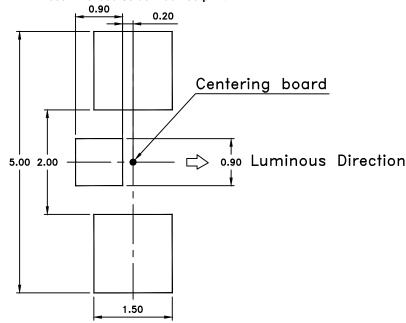
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#### **PACKAGE DIMENSIONS** in millimeters: **VEMD10940F**



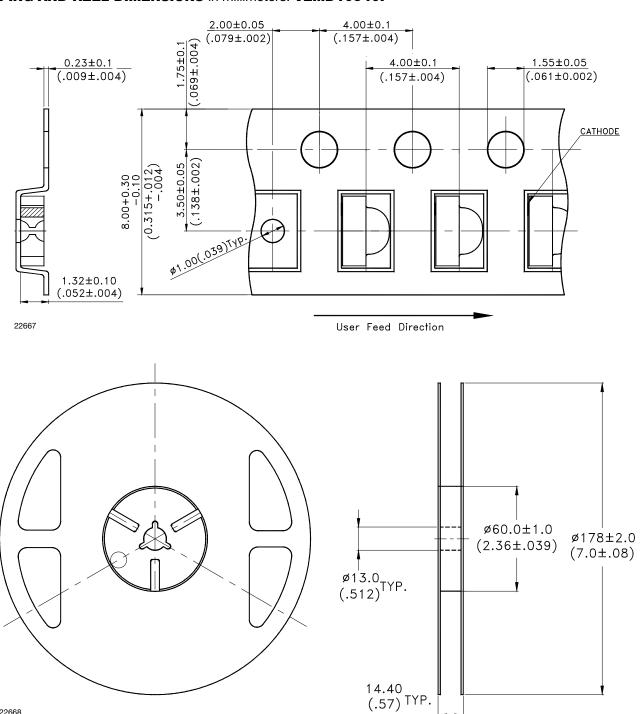
#### **Recommended Solder Pad Footprint**



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### TAPING AND REEL DIMENSIONS in millimeters: VEMD10940F





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